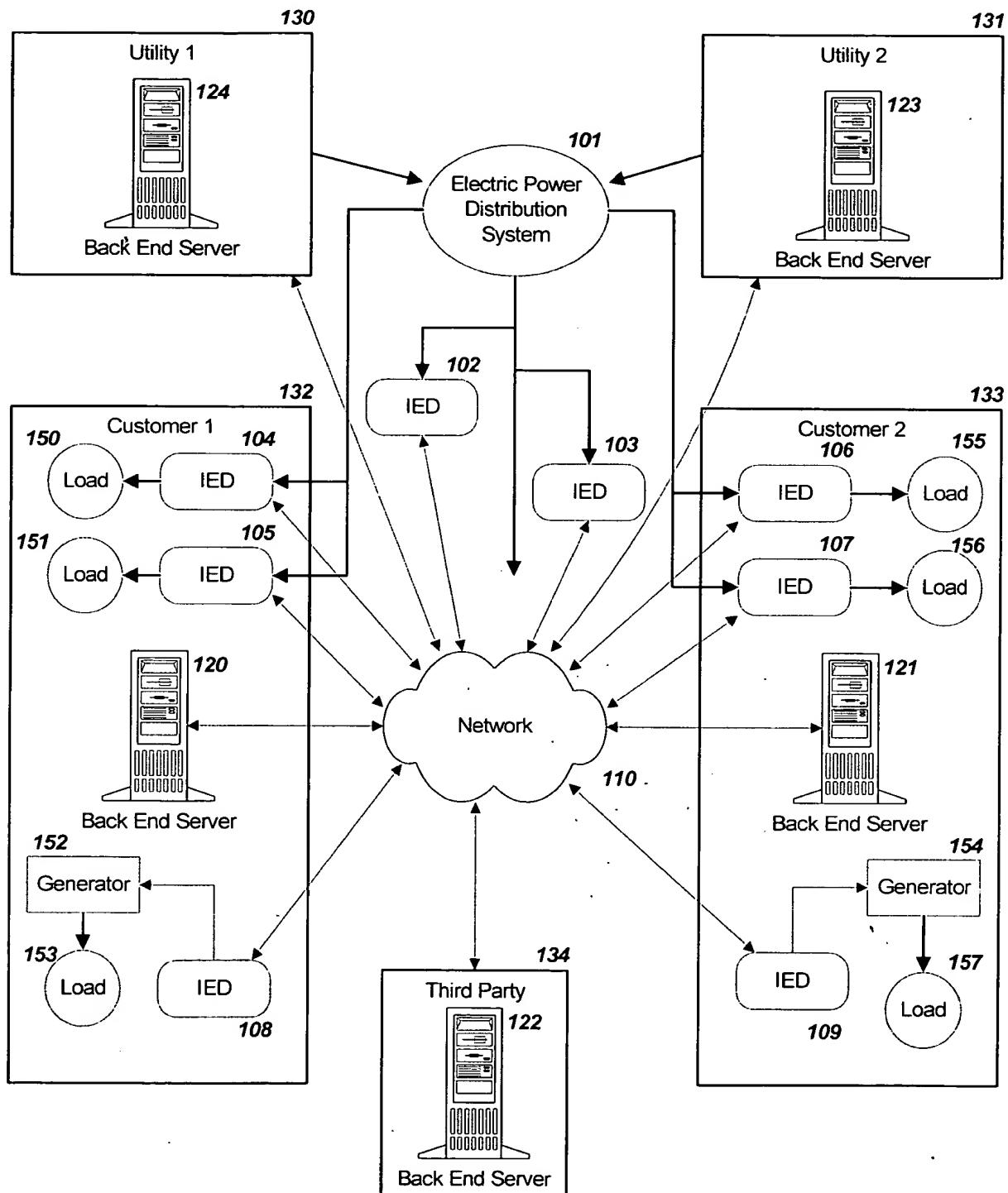


09/896,570

100

Figure 1



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Figure 2a

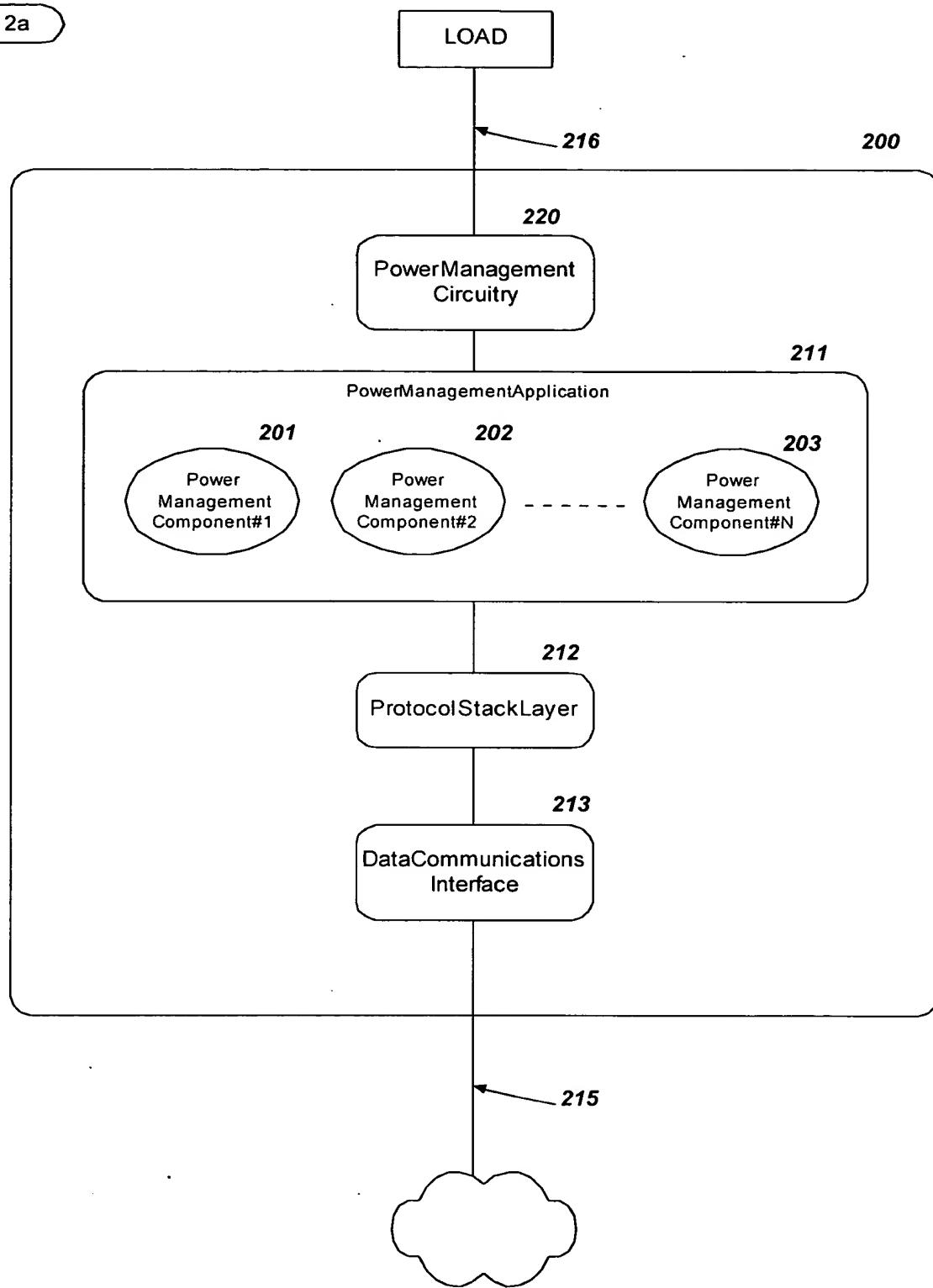
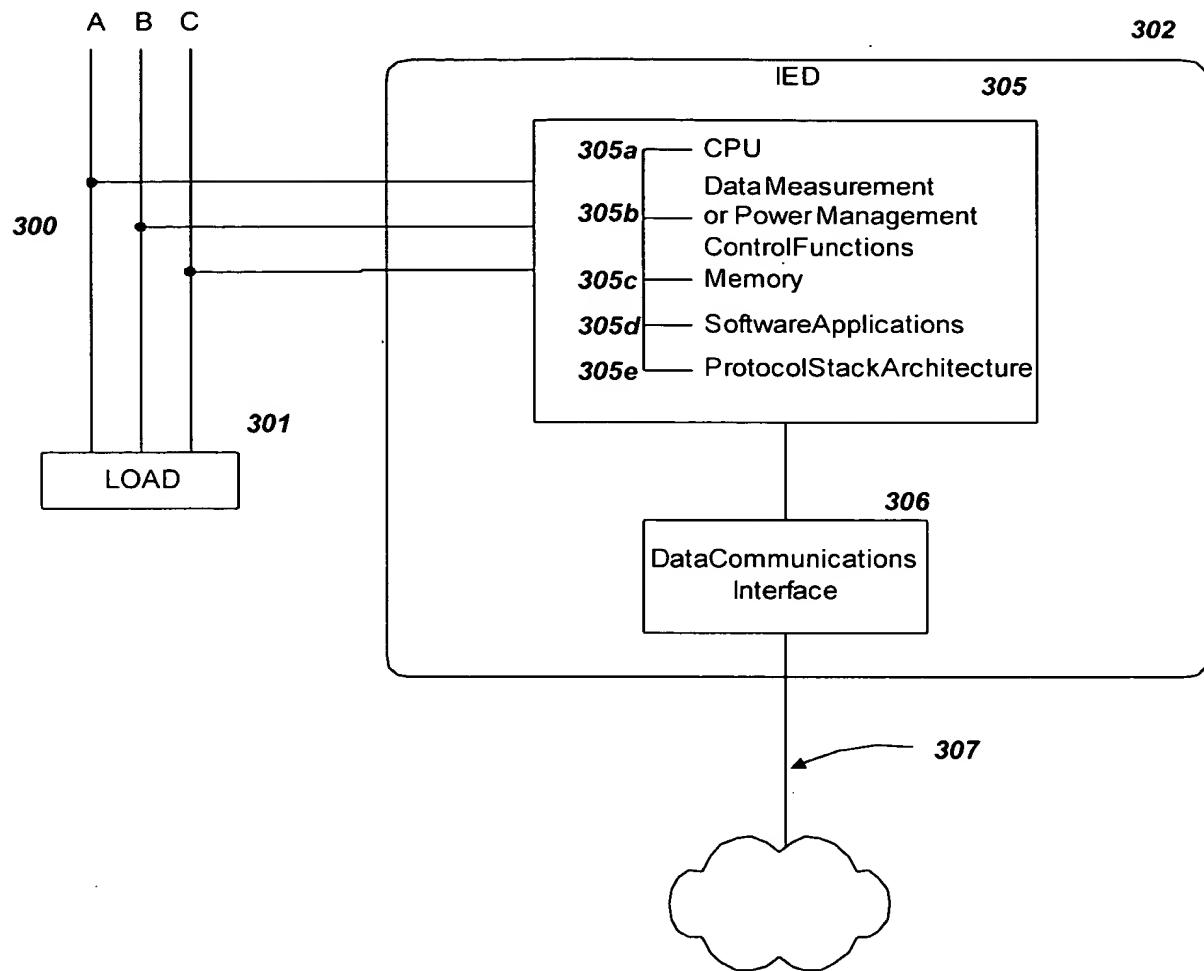


Figure 3a



317

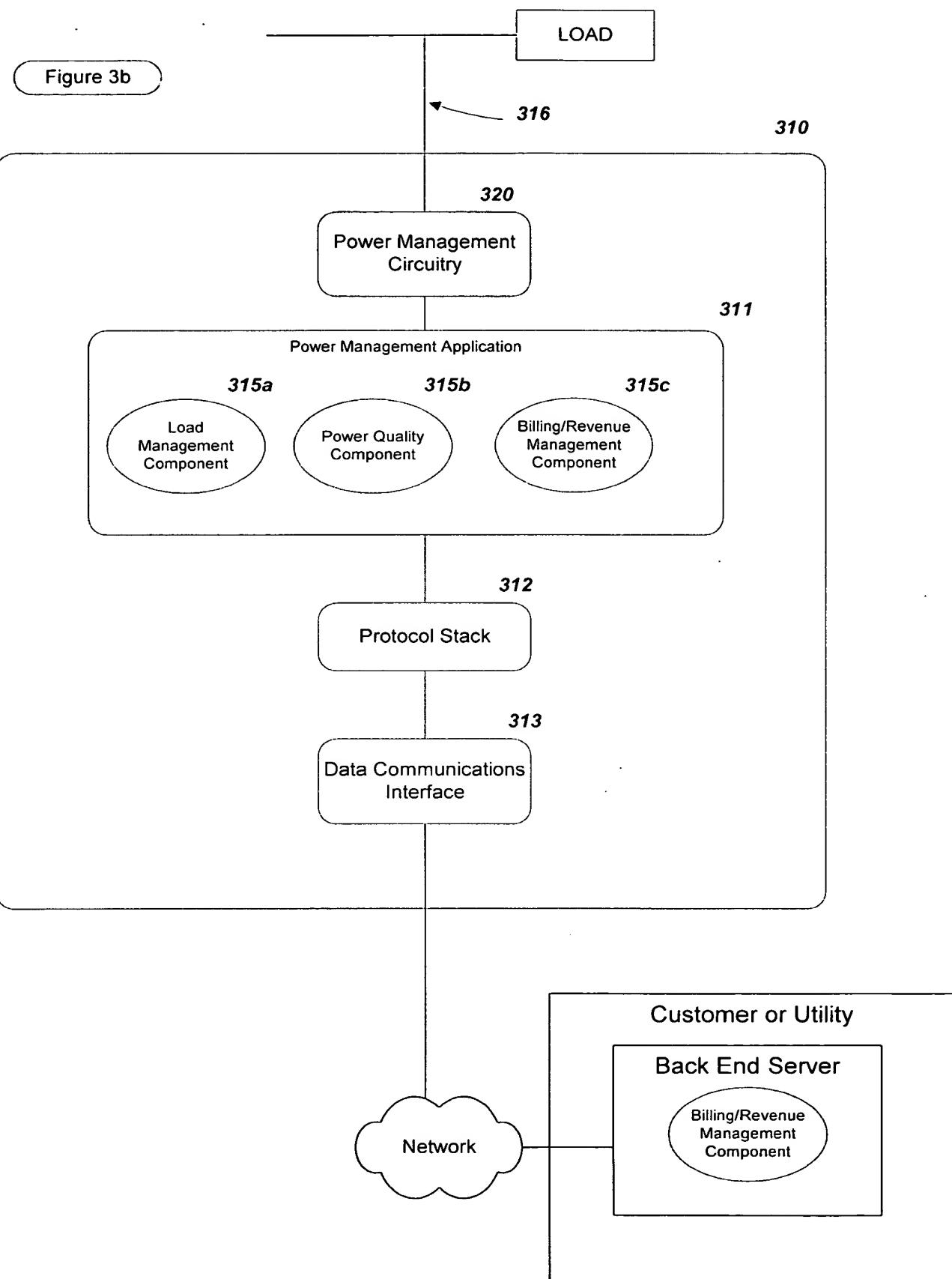


Figure 3c

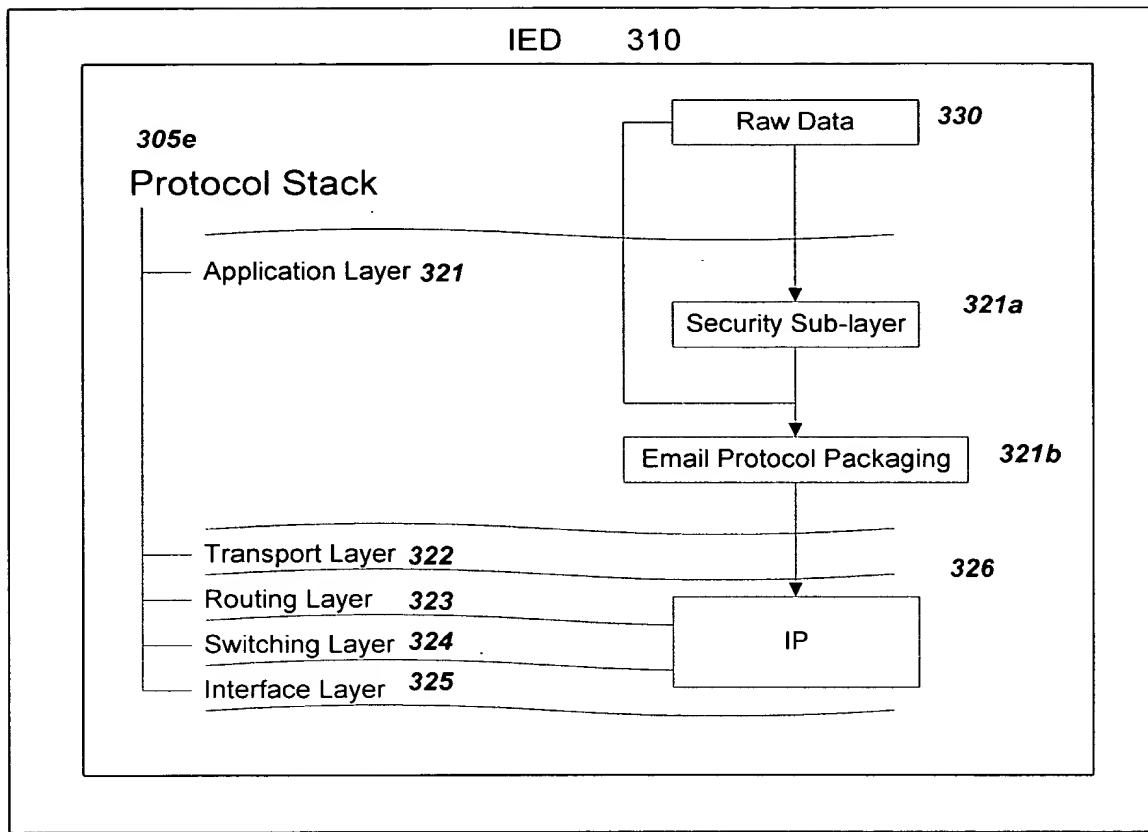


Figure 4a

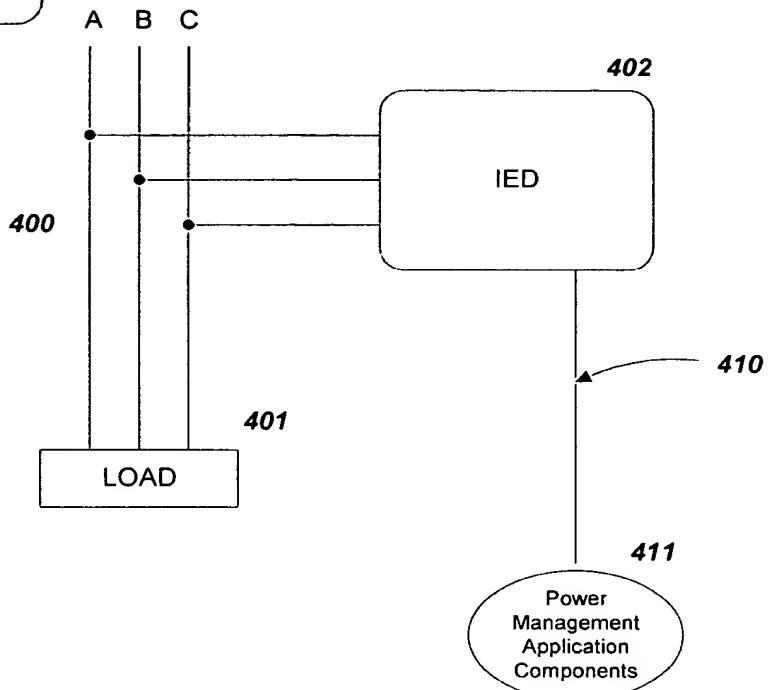
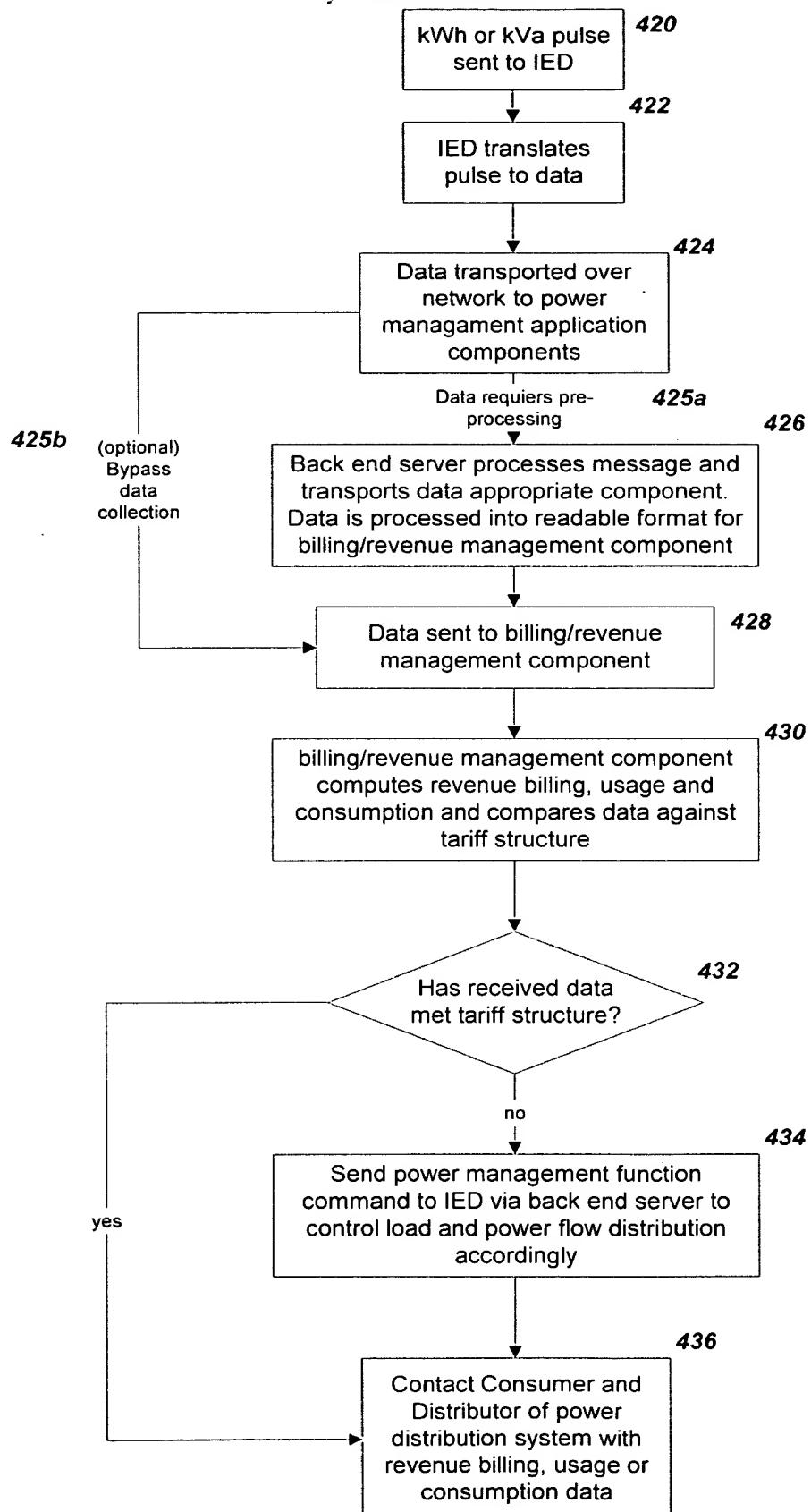


Figure 4b



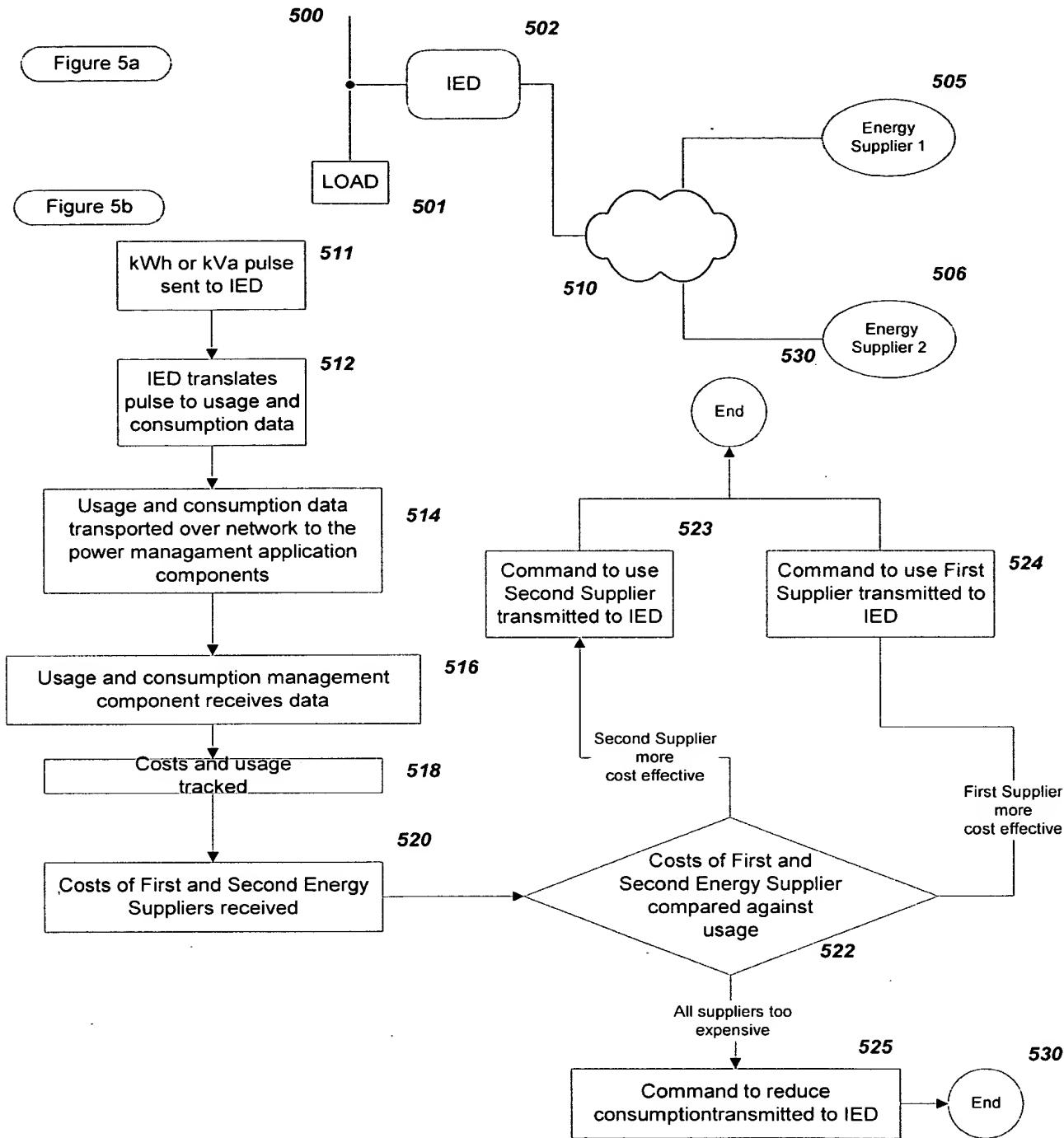


Figure 6

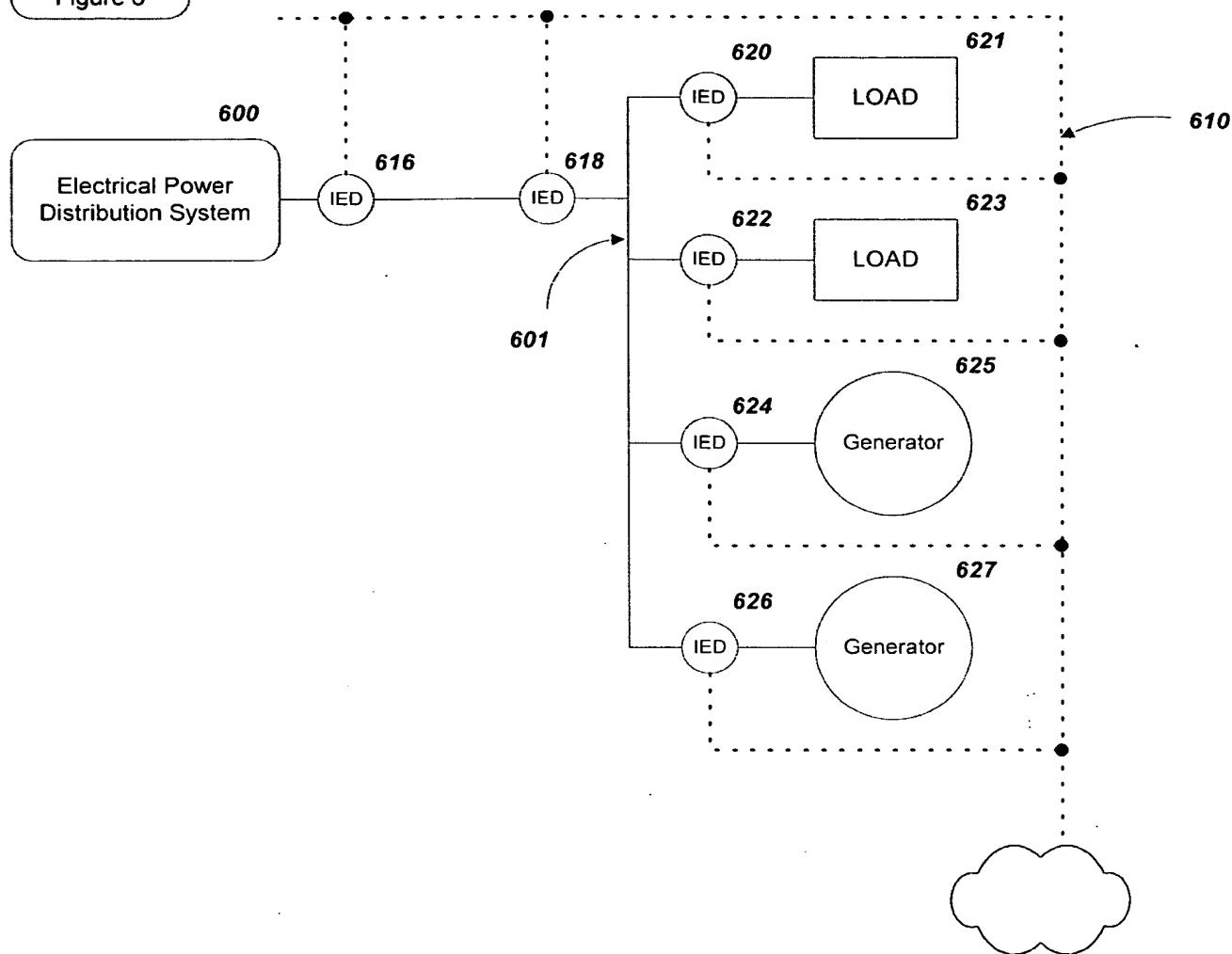


Figure 7

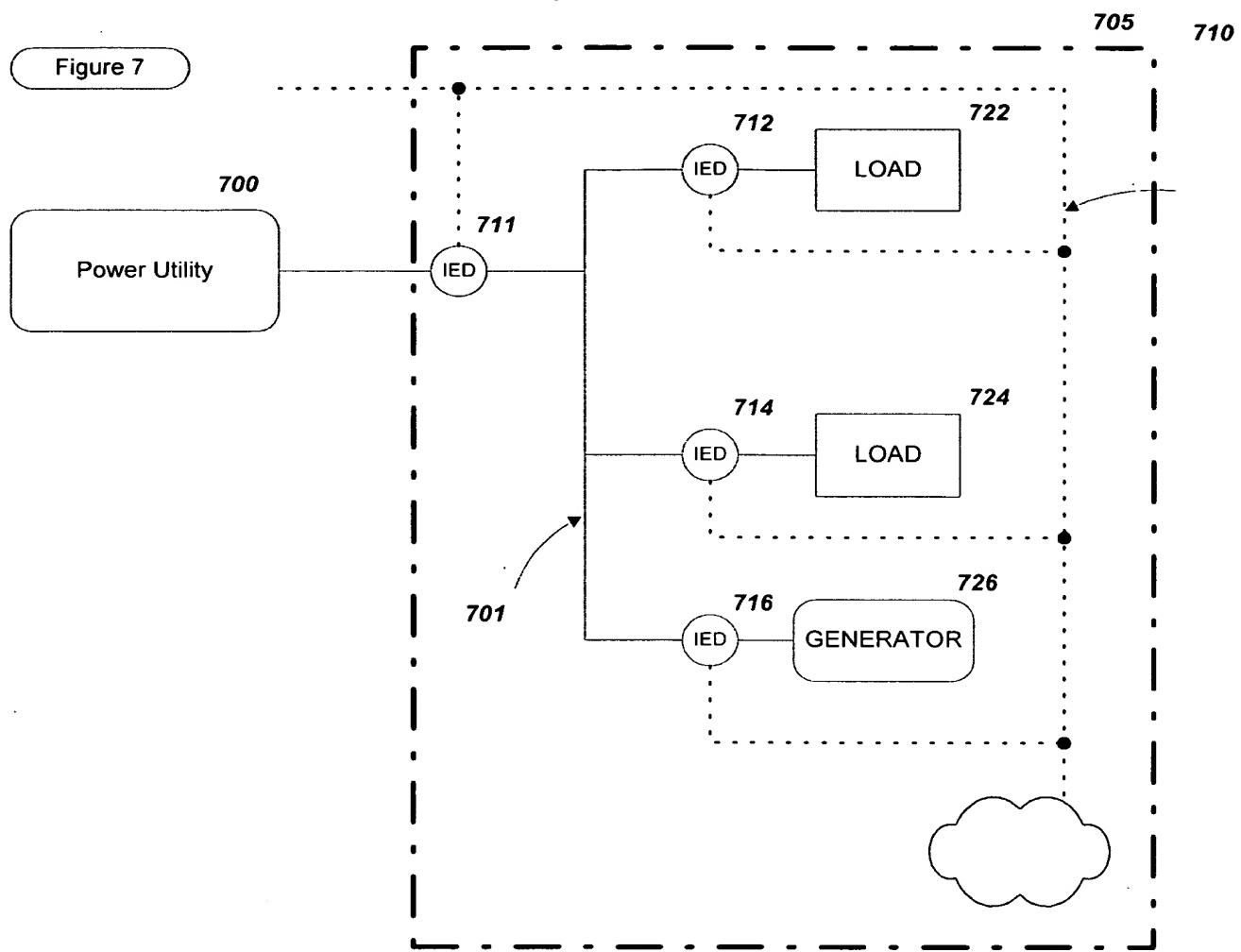


Figure 8

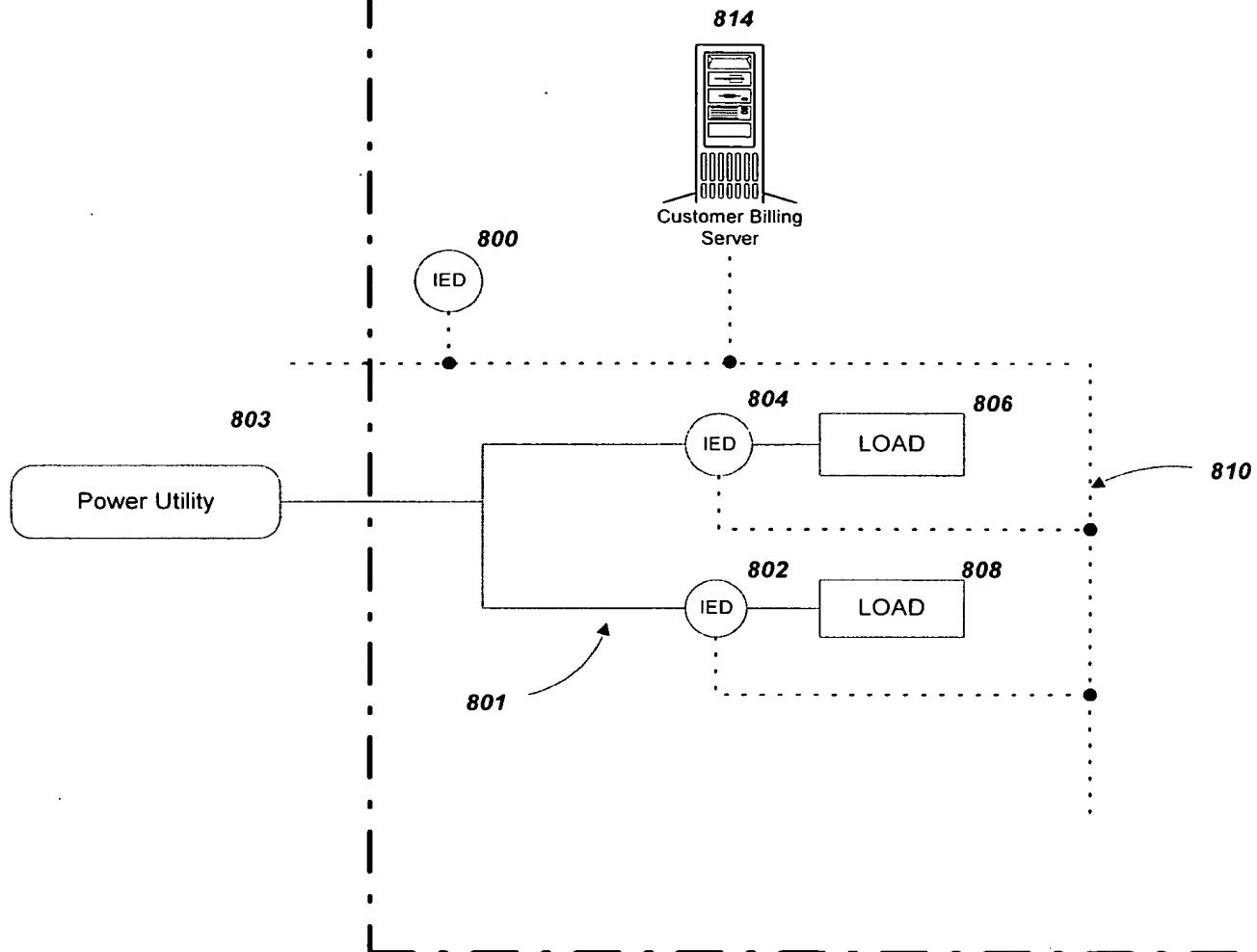


Figure 9

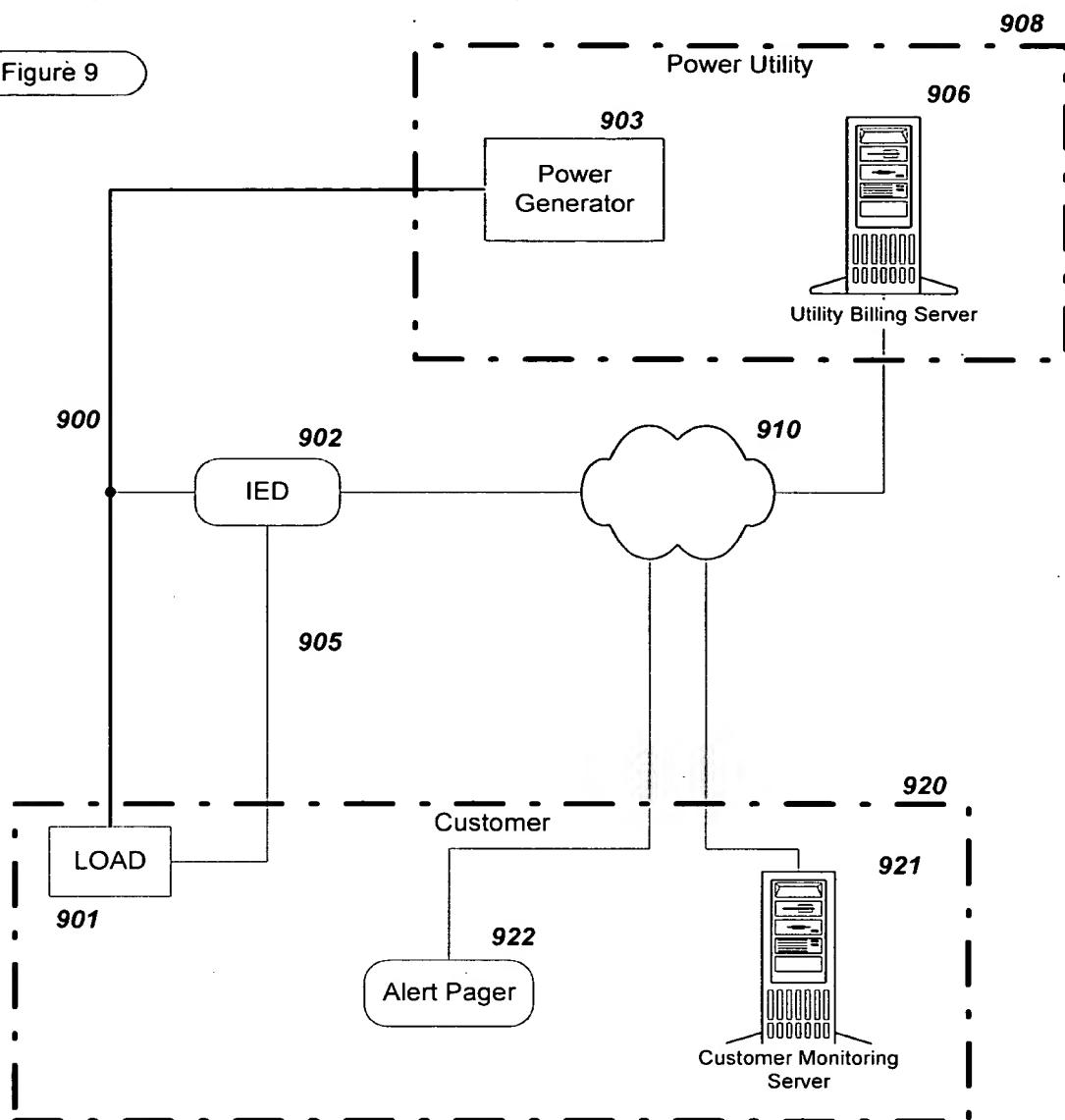
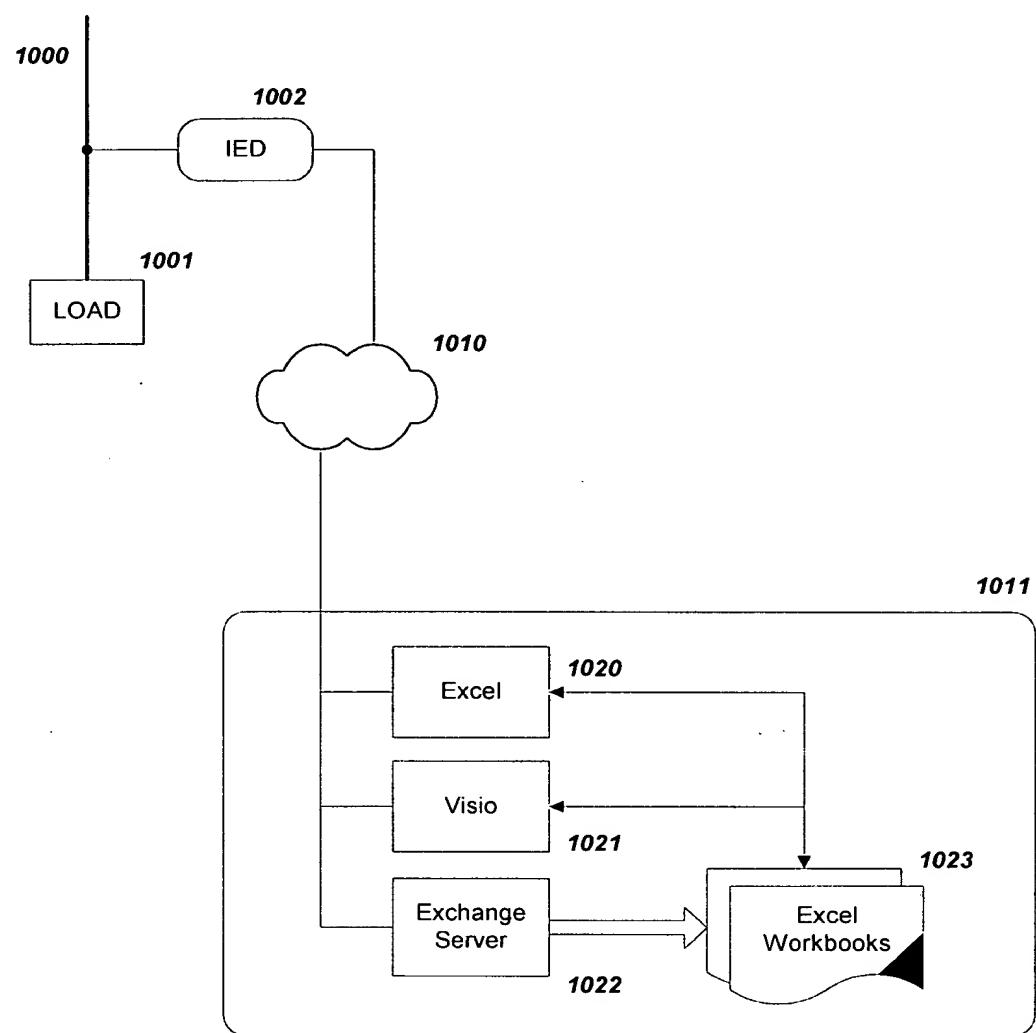


Figure 10



Default Diagram

LABEL	VALUE
la	197.97
lb	207.52
lc	237.82
V\ln c	479.28
V\ln b	371.46
V\ln ca	580.46
V\ln ab	589.1
V\ln bc	586.28
V\ln avg	585.28
V\ln avg	357.23
l avg	214.44
PF sign tot	.94
Freq	59
CL1 LocalTime	08:32:39

Site 1.a8500

Inventor(s): Andrew W. Blackett, Michael E. Teachman
Attorney Docket No.: 6270/64

Patent Application for COMMUNICATIONS ARCHITECTURE FOR INTELLIGENT ELECTRONIC DEVICES

Attorney Document No.: 6270/64

Sum of Currents:

643.31
OVER 550 Volts

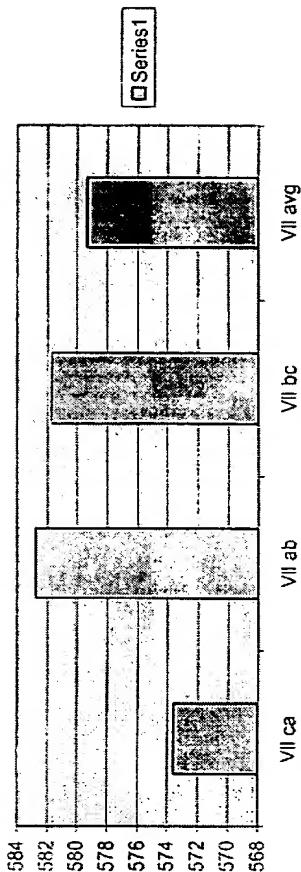


Figure 11

Figure 12

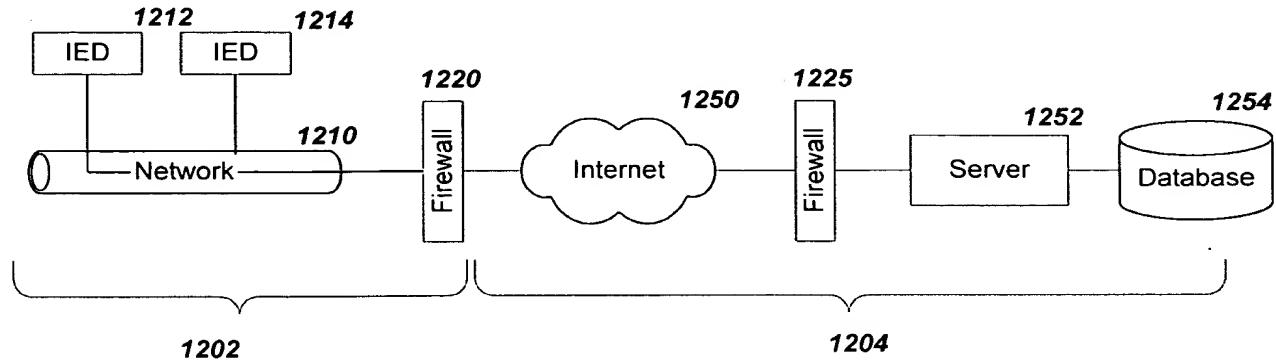


Figure 13

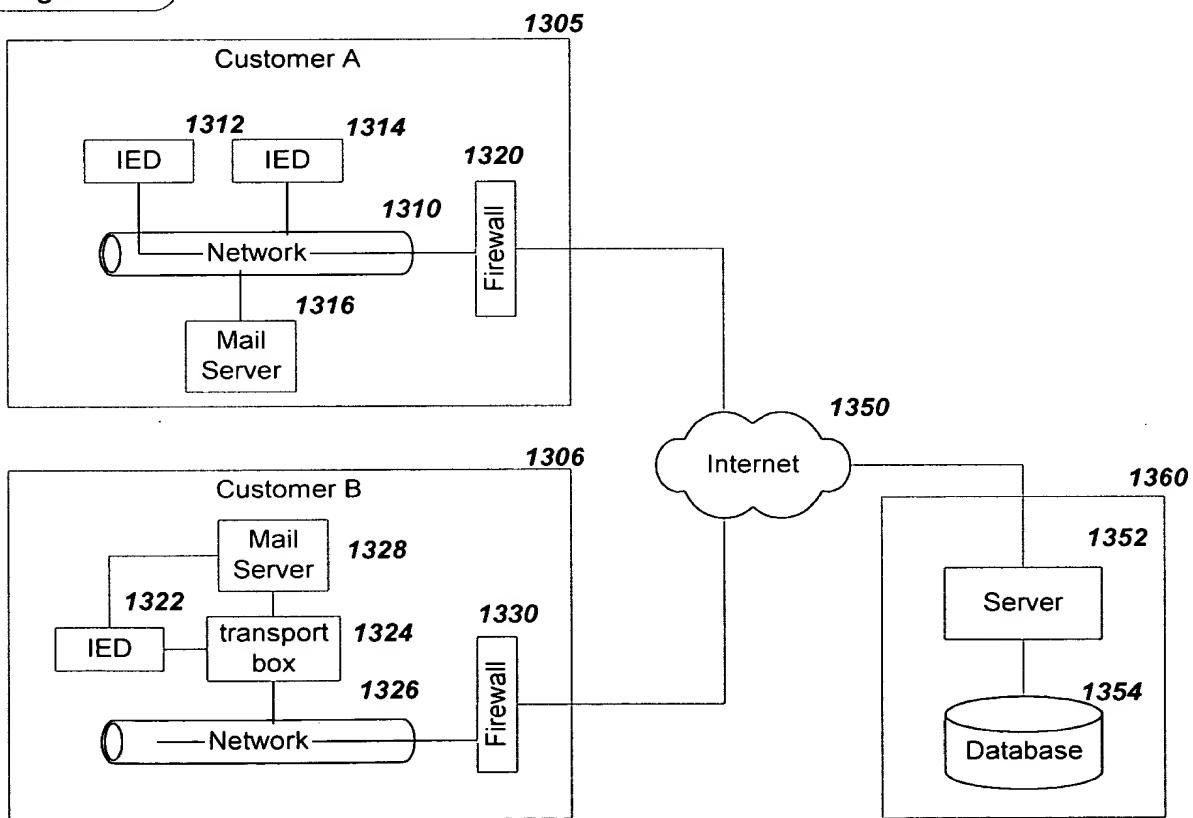


Figure 14

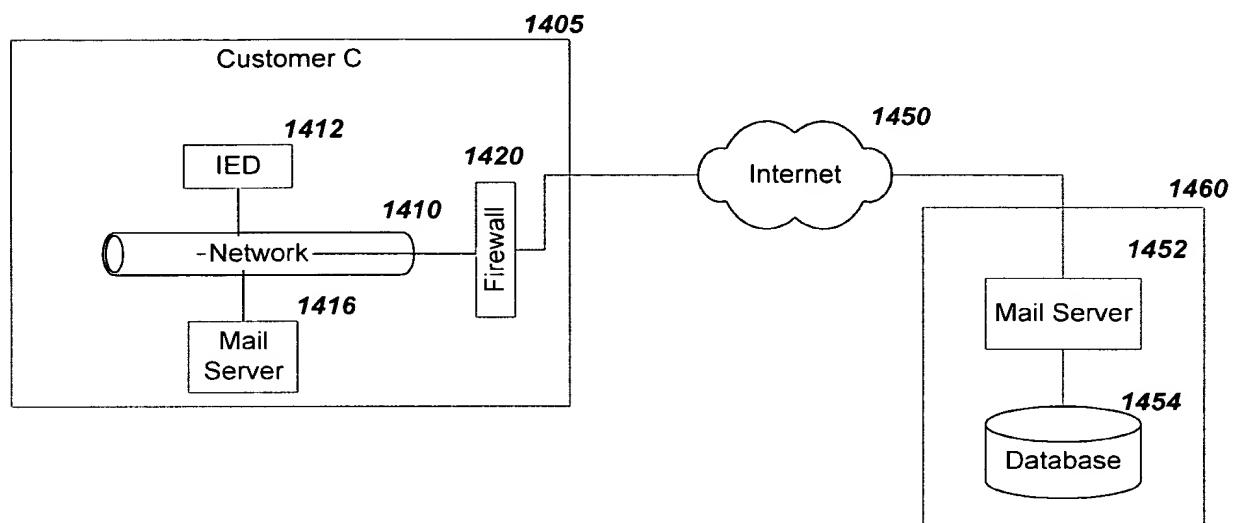


Figure 15a

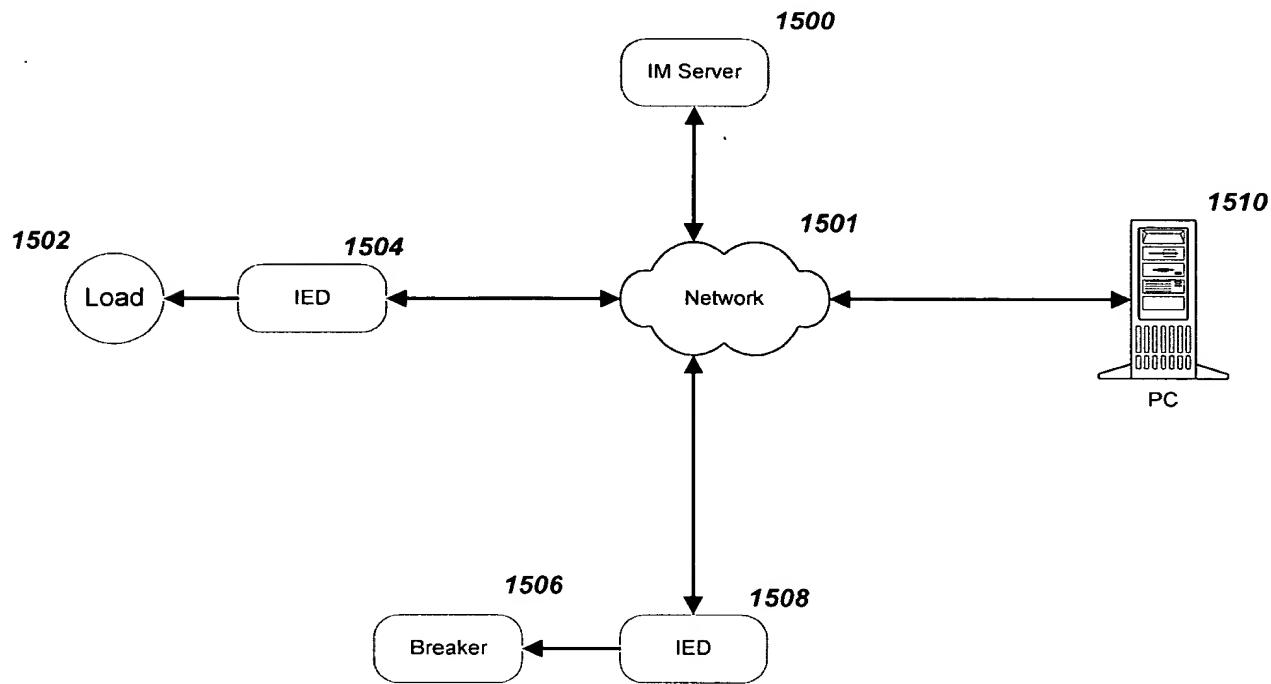


Figure 15b

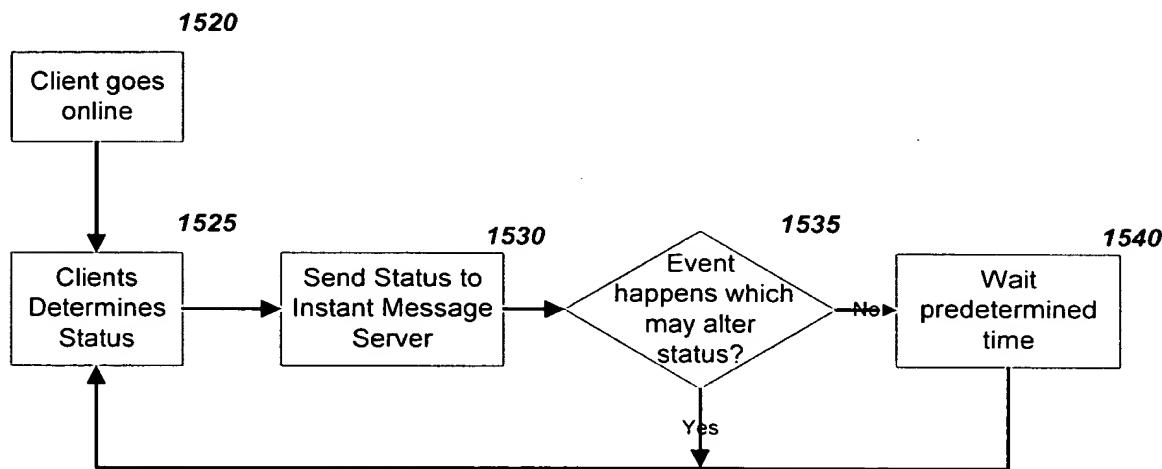


Figure 15c

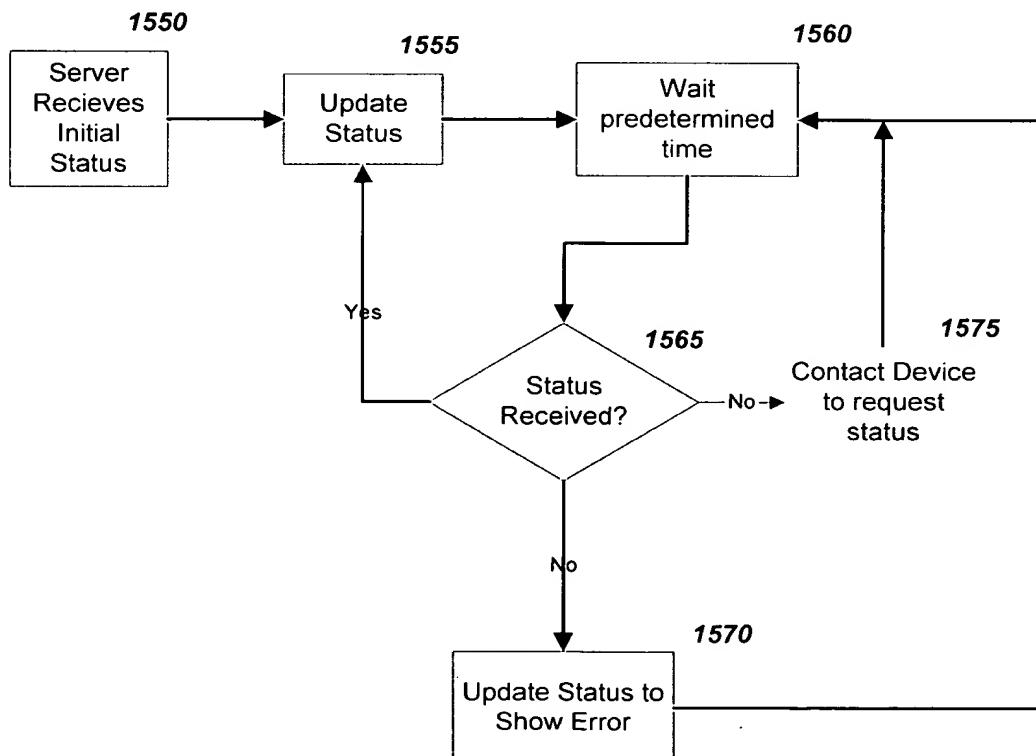


Figure 16

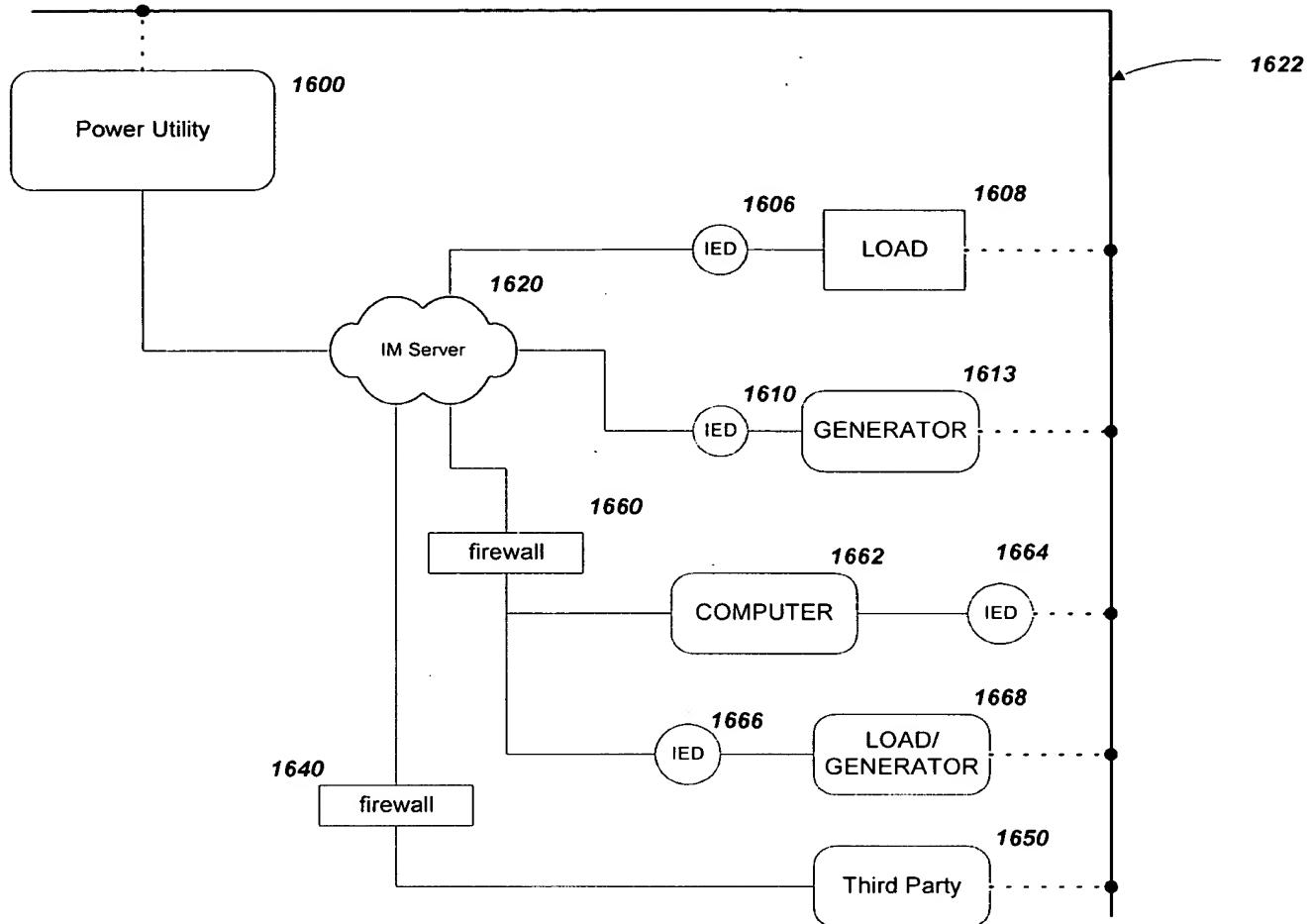


Figure 17

